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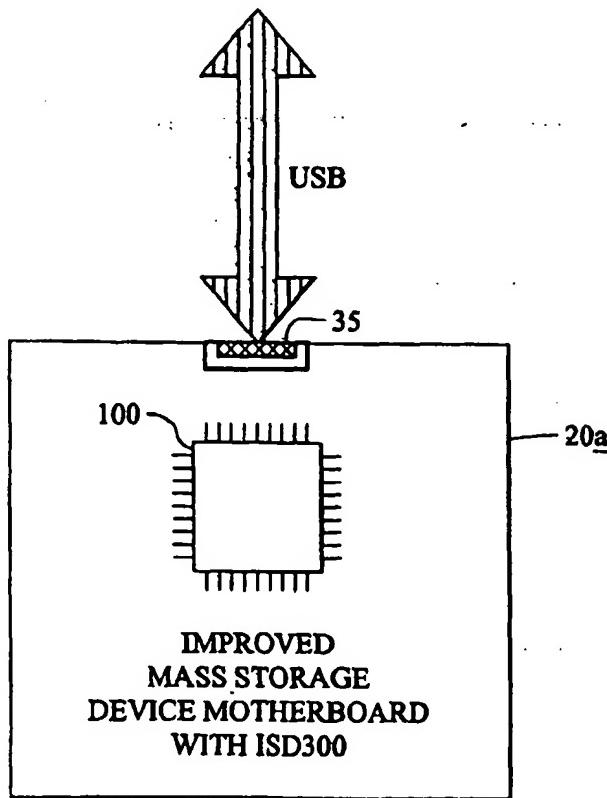
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[Continued on next page]

(54) Title: UNIVERSAL SERIAL BUS (USB) INTERFACE FOR MASS STORAGE DEVICE



(57) Abstract: A mass storage device motherboard or secondary board includes a bridging circuit. The bridging circuit converts signals from the mass storage device into USB signals. The bridging circuit can be provided by a chip that converts ATA/ATAPI signals into USB signals.

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INTERNATIONAL SEARCH REPORT

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A. CLASSIFICATION OF SUBJECT MATTER
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According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
IPC 7 G06F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, PAJ, WPI Data

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	EP 0 890 905 A (SHUTTLE TECHNOLOGY LIMITED) 13 January 1999 (1999-01-13) abstract column 1, line 43 -column 1, line 52 column 2, line 47 -column 2, line 56 column 3, line 20 -column 3, line 30 column 5, line 14 -column 6, line 43 column 7, line 9 -column 8, line 11 ---	1-20
P, X	US 6 199 122 B1 (KOBAYASHI TOSHIYA) 6 March 2001 (2001-03-06) column 2, line 38 -column 3, line 62 abstract figure 1	1, 4, 5, 9-12, 18, 20
X	& JP 11 053485 A (TOKYO ELECTRON LTD) 26 February 1999 (1999-02-26) ---	-/-

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Patent family members are listed in annex.

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'&' document member of the same patent family

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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

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INTERNATIONAL SEARCH REPORT

Information on patent family members

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